

[illegible]

```
LL      NN      NN      KK      KK      VV      VV      MM      MM      CCCCCCCC  TTTTTTTTTT  RRRRRRRR  LL
LL      NN      NN      KK      KK      VV      VV      MM      MM      CCCCCCCC  TTTTTTTTTT  RRRRRRRR  LL
LL      NN      NN      KK      KK      VV      VV      MM      MM      CC          TT          RR          LL
LL      NN      NN      KK      KK      VV      VV      MM      MM      CC          TT          RR          LL
LL      NNNN     NN      KK      KK      VV      VV      MM      MM      CC          TT          RR          LL
LL      NNNN     NN      KK      KK      VV      VV      MM      MM      CC          TT          RR          LL
LL      NN  NN  NN  KKKKKK  KK      VV      VV      MM      MM      CC          TT          RRRRRRRR  LL
LL      NN  NN  NN  KKKKKK  KK      VV      VV      MM      MM      CC          TT          RRRRRRRR  LL
LL      NN      NNNN  KK      KK      VV      VV      MM      MM      CC          TT          RR  RR  LL
LL      NN      NNNN  KK      KK      VV      VV      MM      MM      CC          TT          RR  RR  LL
LL      NN      NN      KK      KK      VV      VV      MM      MM      CC          TT          RR  RR  LL
LL      NN      NN      KK      KK      VV      VV      MM      MM      CC          TT          RR  RR  LL
LLLLLLLLLLL  NN      NN      KK      KK      VV      VV      MM      MM      CCCCCCCC  TT          RR          LLLLLLLLLLL  ....
LLLLLLLLLLL  NN      NN      KK      KK      VV      VV      MM      MM      CCCCCCCC  TT          RR          LLLLLLLLLLL  ....
```

```
LL      IIIIII  SSSSSSSS
LL      IIIIII  SSSSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SS
LL      II      SSSSSS
LL      II      SSSSSS
LL      II      SS
LL      II      SS
LL      II      SS
LLLLLLLLLLL  IIIIII  SSSSSSSS
LLLLLLLLLLL  IIIIII  SSSSSSSS
```

```
1 0001 0 MODULE LNK_VMCTRL ( ! CONTROL FOR ALLOCATION OF VIRTUAL MEMORY
2 0002 0 IDENT = 'V04-000',
3 0003 0 ADDRESSING_MODE(EXTERNAL=GENERAL,
4 0004 0 NONEXTERNAL=LONG_RELATIVE)
5 0005 0 ) =
6 0006 1 BEGIN
7 0007 1
8 0008 1 *****
9 0009 1 *
10 0010 1 * COPYRIGHT (c) 1978, 1980, 1982, 1984 BY
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26 0026 1 * SOFTWARE ON EQUIPMENT WHICH IS NOT SUPPLIED BY DIGITAL.
27 0027 1 *
28 0028 1 *
29 0029 1 *****
30 0030 1
31 0031 1 ++
32 0032 1 FACILITY: LINKER
33 0033 1
34 0034 1 ABSTRACT: THIS MODULE CONTAINS THE ROUTINES TO ALLOCATE VIRTUAL MEMORY
35 0035 1 AT END OF PASS 1.
36 0036 1
37 0037 1
38 0038 1 ENVIRONMENT: VMS NATIVE MODE
39 0039 1
40 0040 1 AUTHOR: T.J. PORTER, CREATION DATE: 15-JUN-78
41 0041 1
42 0042 1 MODIFIED BY:
43 0043 1
44 0044 1 V03-001 BLS0007 Benn Schreiber, 3-Jun-1980
45 0045 1 Convert to MDL data structures.
46 0046 1 --
```



```

48      0047 1 1
49      0048 1 1++
50      0049 1 1      FUNCTIONAL DESCRIPTION
51      0050 1 1
52      0051 1 1      LIBRARY
53      0052 1 1      'STARLETL32';
54      0053 1 1      REQUIRE
55      0054 1 1      'PREFIX';
56      0169 1 1      LIBRARY
57      0170 1 1      'DATBAS';
58      0171 1 1
59      0172 1 1      EXTERNAL ROUTINE
60      0173 1 1      LNK$ALLOBLK : NOVALUE,      ! DYNAMIC MEMORY ALLOCATOR
61      0174 1 1      LNK$DEALBLK : NOVALUE;      ! AND DEALLOCATOR
62      0175 1 1
63      0176 1 1      EXTERNAL
64      0177 1 1      LNK$GL_FVMLST;      ! LISTHEAD OF FREE VIR MEM DESCRIPTORS
65      0178 1 1
66      0179 1 1      GLOBAL
67      0180 1 1      LNK$GL_MINVA : INITIAL(MAX_ADDRESS),      ! LOWEST ADDRESS ALLOCATED
68      0181 1 1      LNK$GL_MAXVA : INITIAL(0);      ! HIGHEST ADDRESS ALLOCATED

```

```
0182 1 GLOBAL ROUTINE LNK$ALLOVIRMEM(ADDRESS,PAGES) =
0183 BEGIN
0184
0185 ++
0186 THIS ROUTINE IS CALLED TO ATTEMPT ALLOCATION OF A SPECIFIC
0187 PIECE OF VIRTUAL MEMORY. THE ADDRESS AND PAGE COUNT REQUIRED
0188 ARE THE INPUT ARGUMENTS. THE ROUTINE RETURNS TRUE OR FALSE
0189 DEPENDING ON WHETHER THE SPECIFIED MEMORY IS AVAILABLE OR
0190 ALREADY ALLOCATED.
0191
0192 THE AVAILABLE VIRTUAL MEMORY IS CONTROLLED BY A SINGLY LINKED
0193 LIST OF BLOCKS WITH FORMAT:
0194
0195 ! NEXT DESCRIPTOR !
0196 ! FREE ADDRESS !
0197 ! # FREE BYTES !
0198
0199
0200
0201 --
0202
0203 LOCAL
0204
0205 BYTES, ! BYTE COUNT REQUESTED
0206 FREBLK : REF BLOCK[.BYTE], ! FREE BLOCK POINTER
0207 PREVBLK : REF BLOCK[.BYTE], ! PREVIOUS FREE BLOCK IN LIST
0208
0209 IF (BYTES = .PAGES * 512) EQL 0 ! IF ZERO SIZE
0210 THEN RETURN TRUE; ! RETURN SUCCESS
0211
0212 PREVBLK = LNK$GL_FVMLST; ! SET AT LISTHEAD
0213 WHILE (FREBLK = .PREVBLK[FVMSL_NXTFVM]) NEQ 0 ! MOVE TO NEXT BLOCK DESCRIPTOR IN LIST
0214 DO IF .ADDRESS LSSU .FREBLK[FVMSL_ADDRESS] ! LOOP TILL WE GET TO ONE FOR REQUESTED MEMORY
0215 OR .ADDRESS GEQU (.FREBLK[FVMSL_ADDRESS] + .FREBLK[FVMSL_BYTES])
0216 THEN PREVBLK = .FREBLK ! ...
0217 ELSE BEGIN
0218 IF (.ADDRESS + .BYTES) GTRU (.FREBLK[FVMSL_ADDRESS] + ! DOES DESIRED BLOCK FIT
0219 .FREBLK[FVMSL_BYTES]) ! IN THE HOLE
0220 THEN EXITLOOP; ! QUIT IF NOT
0221 IF .ADDRESS EQL .FREBLK[FVMSL_ADDRESS] ! IF AT FRONT OF HOLE
0222 THEN IF (FREBLK[FVMSL_BYTES] = .FREBLK[FVMSL_BYTES] - ! REDUCE THE SIZE OF REMAINDER
0223 .BYTES) EQL 0 ! AND IF GOING TO ZERO
0224 THEN BEGIN
0225 PREVBLK[FVMSL_NXTFVM] = .FREBLK[FVMSL_NXTFVM]; ! TAKE DESCRIPTOR OFF LIST
0226 LNK$DEALBLK(FVMSL_SIZE,.FREBLK); ! AND DEALLOCATE IT
0227 END
0228 ELSE FREBLK[FVMSL_ADDRESS] = .FREBLK[FVMSL_ADDRESS] + .BYTES ! SET ADDRESS OF REMAINDER
0229 ELSE BEGIN ! NOT AT THE FRONT
0230 IF (.FREBLK[FVMSL_ADDRESS] + .FREBLK[FVMSL_BYTES]) EQL ! CHECK IF IT IS ON END
0231 (.ADDRESS + .BYTES) ! OF THE BLOCK
0232 THEN FREBLK[FVMSL_BYTES] = .FREBLK[FVMSL_BYTES] - .BYTES ! IF SO JUST REDUCE SIZE
0233 ELSE BEGIN
0234 LOCAL NXTBLK : REF BLOCK[.BYTE]; ! OTHERWISE NEED TO SPLIT THE BLOCK
0235 LNK$ALLOBLK(FVMSL_SIZE,NXTBLK); ! ALLOCATE ANOTHER DESCRIPTOR
0236 NXTBLK[FVMSL_NXTFVM] = .FREBLK[FVMSL_NXTFVM]; ! WHICH POINTS ONWARD
0237 FREBLK[FVMSL_NXTFVM] = .NXTBLK; ! AND POINTED TO BY CURRENT
0238 NXTBLK[FVMSL_ADDRESS] = .ADDRESS + .BYTES; ! DESCRIBES THE REMNANT AT END
0239 NXTBLK[FVMSL_BYTES] = .FREBLK[FVMSL_BYTES] -
```

LNK_VMCTRL
V04=000

G 4
16-Sep-1984 00:39:52 VAX-11 Bliss-32 V4.0-742
14-Sep-1984 12:40:38 [LINKER.SRC]LNKVMCTRL.B32;1

Page 4
(3)

```

: 127      0239      6      (.NXTBLK[FVMSL_ADDRESS] -
: 128      0240      5      .FREBLK[FVMSL_ADDRESS]);
: 129      0241      5      FREBLK[FVMSL_BYTES] = .ADDRESS - .FREBLK[FVMSL_ADDRESS];      ! AND SET CURRENT FR
: 130      0242      5      END;
: 131      0243      5      END;
: 132      0244      5      IF (.ADDRESS + .BYTES - 1) GTRU .LNK$GL_MAXVA      ! MAXIMIZE THE ADDRESS
: 133      0245      5      THEN LNK$GL_MAXVA = .ADDRESS + .BYTES = 1;      ! SPACE ALLOCATED
: 134      0246      5      IF .ADDRESS - LSSU .LNK$GL_MINVA      ! AND RESET MINIMUM IF NEXESSARY
: 135      0247      5      THEN LNK$GL_MINVA = .ADDRESS;
: 136      0248      5      RETURN TRUE;
: 137      0249      5      END;
: 138      0250      5      RETURN FALSE;
: 139      0251      1      END;      ! OF LNK$ALLOVIRMEM
```

```

                                .TITLE LNK_VMCTRL
                                .IDENT \V04-000\
                                .PSECT $GLOBALS,NOEXE,2
                                C0000000 00000 LNK$GL_MINVA::
                                .LONG -1073741824
                                00000000 00004 LNK$GL_MAXVA::
                                .LONG 0
                                .EXTRN LNK$ALLOBLK, LNK$DEALBLK
                                .EXTRN LNK$GL_FVMLST
                                .PSECT $CODE$,NOWRT,2
                                .ENTRY LNK$ALLOVIRMEM, Save R2,R3,R4,R5,R6,R7,R8 : 0182
                                MOVAB LNK$GL_MAXVA, R8
                                SUBL2 #4, SP
                                ASHL #9, PAGES, BYTES : 0208
                                BNEQ 1$
                                BRW 11$
                                MOVAB LNK$GL_FVMLST, PREVBLK : 0211
                                MOVL ADDRESS, R4 : 0213
                                MOVL (PREVBLK), FREBLK : 0212
                                BNEQ 3$
                                BRW 12$
                                CMPL R4, 4(FREBLK) : 0213
                                BLSSU 4$
                                ADDL3 8(FREBLK), 4(FREBLK), R1 : 0214
                                CMPL R4, R1
                                BLSSU 5$
                                MOVL FREBLK, PREVBLK : 0215
                                BRB 2$
                                ADDL3 BYTES, R4, R5 : 0217
                                MOVAB 8(FREBLK), R3 : 0218
                                ADDL3 (R3), 4(FREBLK), R1
                                CMPL R5, R1 : 0217
                                BGTRU 12$
                                CMPL R4, 4(FREBLK) : 0220
                                BNEQ 7$
                                SUBL2 BYTES, (R3) : 0222
                                BNEQ 6$
```

56	08	AC	00000000	01FC 00000	EF 9E 00002	
		5E			04 C2 00009	
		AC			09 78 0000C	
					03 12 00011	
				00A4	31 00013	
		57	00000000G	00	9E 00016	1\$:
		54	04	AC	D0 0001D	
		52		67	D0 00021	2\$:
				03	12 00024	
				0095	31 00026	
	04	A2		54	D1 00029	3\$:
				0B	1F 0002D	
51	04	A2	08	A2	C1 0002F	
		51		54	D1 00035	
				05	1F 00038	
		57		52	D0 0003A	4\$:
				E2	11 0003D	
55		54		56	C1 0003F	5\$:
		53	08	A2	9E 00043	
51	04	A2		63	C1 00047	
		51		55	D1 0004C	
				6D	1A 0004F	
	04	A2		54	D1 00051	
				1B	12 00055	
		63		56	C2 00057	
				10	12 0005A	

LNK_VMCTRL
V04=000

H 4
16-Sep-1984 00:39:52
14-Sep-1984 12:40:38

VAX-11 Bliss-32 V4.0-742
[LINKER.SRC]LNKVMCTRL.B32;1

Page 5
(3)

		67		62	D0 0005C	MOVL	(FREBLK), (PREVBLK)	:	0224
				52	DD 0005F	PUSHL	FREBLK	:	0225
				0C	DD 00061	PUSHL	#12	:	
	00000000G	00		02	FB 00063	CALLS	#2, LNK\$DEALBLK	:	
				38	11 0006A	BRB	9\$:	0221
	04	A2		56	C0 0006C 6\$:	ADDL2	BYTES, 4(FREBLK)	:	0227
				32	11 00070	BRB	9\$:	0221
		55		51	D1 00072 7\$:	CMPL	R1, R5	:	0230
				05	12 00075	BNEQ	8\$:	
		63		56	C2 00077	SUBL2	BYTES, (R3)	:	0231
				28	11 0007A	BRB	9\$:	
				5E	DD 0007C 8\$:	PUSHL	SP	:	0234
				0C	DD 0007E	PUSHL	#12	:	
	00000000G	00		02	FB 00080	CALLS	#2, LNK\$ALLOBLK	:	
		50		6E	D0 00087	MOVL	NXTBLK, R0	:	0235
		60		62	D0 0008A	MOVL	(FREBLK), (R0)	:	
		62		50	D0 0008D	MOVL	R0, (FREBLK)	:	0236
		04	A0	55	D0 00090	MOVL	R5, 4(R0)	:	0237
	08	51	04	A0	C3 00094	SUBL3	4(R0), 4(FREBLK), R1	:	0240
		04	A2	63	C1 0009A	ADDL3	(R3), R1, 8(R0)	:	0239
			51	A2	C3 0009F	SUBL3	4(FREBLK), R4, (R3)	:	0241
			54	A5	9E 000A4 9\$:	MOVAB	-1(R5), R0	:	0244
			50	D1 000A8	CMPL	R0, LNK\$GL_MAXVA		:	
			68	03	1B 000AB	BLEQU	10\$:	
				50	D0 000AD	MOVL	R0, LNK\$GL_MAXVA	:	0245
		FC	A8	54	D1 000B0 10\$:	CMPL	R4, LNK\$GL_MINVA	:	0246
				04	1E 000B4	BGEQU	11\$:	
		FC	A8	54	D0 000B6	MOVL	R4, LNK\$GL_MINVA	:	0247
			50	01	D0 000BA 11\$:	MOVL	#1, R0	:	0248
					04 000BD	RET		:	
				50	D4 000BE 12\$:	CLRL	R0	:	0250
				04	000C0	RET		:	0251

; Routine Size: 193 bytes, Routine Base: \$CODE\$ + 0000

```
141 0252 1 GLOBAL ROUTINE LNK$FINDVIRMEM(RETADR,PAGES,LOWESTVA) =
142 0253 2 BEGIN
143 0254
144 0255 ++
145 0256 THIS ROUTINE IS SIMILAR TO LNK$ALLOVIRMEM EXCEPT THAT ANY FREE
146 0257 VIRTUAL MEMORY LARGE ENOUGH IS ALLOCATED, STARTING AT LOWEST
147 0258 ADDRESS END.
148 0259
149 0260 IF LOWESTVA IS SUPPLIED IT SPECIFIES THAT THE VIRTUAL MEMORY
150 0261 ALLOCATED MUST BE AT A HIGHER ADDRESS THAN LOWESTVA
151 0262
152 0263 RETADR = ADDRESS OF CELL TO RECEIVE THE ADDRESS OF
153 0264 THE FIRST BYTE ALLOCATED.
154 0265
155 0266 --
156 0267 MAP
157 0268 RETADR : REF VECTOR[,LONG];
158 0269
159 0270 BUILTIN
160 0271 NULLPARAMETER;
161 0272
162 0273 LOCAL
163 0274 BYTES,
164 0275 FREBLK : REF BLOCK[,BYTE],
165 0276 PREVBLK : REF BLOCK[,BYTE],
166 0277 SPECIALADR;
167 0278
168 0279 IF (BYTES = .PAGES * 512) EQL 0
169 0280 THEN BEGIN
170 0281 RETADR[0] = 0; ! IF REQUESTED SIZE IS ZERO
171 0282 RETURN TRUE ! SUCCESS
172 0283 END;
173 0284
174 0285 SPECIALADR = NOT NULLPARAMETER(3) ! SET FLAG IF TO ALLOCATE ABOVE SPECIFIC ADDRESS
175 0286 AND (.LOWESTVA NEQ 0);
176 0287
177 0288 PREVBLK = LNK$GL_FVMSLST; ! SET TO START OF LIST
178 0289 WHILE (FREBLK = .PREVBLK[FVMSL_NXTFVM]) NEQ 0 ! GO DOWN LIST LOOKING FOR LARGE ENOUGH PIECE OF FRE
179 0290 DO IF (IF .SPECIALADR
180 0291 THEN ((.FREBLK[FVMSL_ADDRESS] LSSU .LOWESTVA)
181 0292 OR ((.FREBLK[FVMSL_ADDRESS]+.FREBLK[FVMSL_BYTES]
182 0293 LSSU .LOWESTVA)
183 0294 OR ((.FREBLK[FVMSL_ADDRESS]+.FREBLK[FVMSL_BYTES]
184 0295 LSSU .LOWESTVA+.BYTES)))
185 0296 ELSE (.BYTES GTRU .FREBLK[FVMSL_BYTES]))
186 0297 THEN PREVBLK = .FREBLK
187 0298 ELSE BEGIN
188 0299 RETADR[0] = .FREBLK[FVMSL_ADDRESS]; ! RETURN ITS VIRTUAL ADDRESS
189 0300 IF (FREBLK[FVMSL_BYTES] = .FREBLK[FVMSL_BYTES] - ! REDUCE ITS SIZE
190 0301 .BYTES) EQL 0 ! AND IF COMPLETELY CONSUMED
191 0302 THEN BEGIN
192 0303 PREVBLK[FVMSL_NXTFVM] = .FREBLK[FVMSL_NXTFVM]; ! REMOVE DESCRIPTOR FROM LIST
193 0304 LNK$DEALBLK(FVMSL_SIZE,.FREBLK); ! AND DEALLOCATE THE DESCRIPTOR
194 0305 END
195 0306 ELSE FREBLK[FVMSL_ADDRESS] = .FREBLK[FVMSL_ADDRESS] + ! OTHERWISE JUST ADJUST THE
196 0307 .BYTES; ! VIRTUAL ADDRESS REMAINING
197 0308 IF (.RETADR[0] + .BYTES - 1) GTRU .LNK$GL_MAXVA ! MAXIMIZE THE ADDRESS
```



```

: 198      0309 3      THEN LNK$GL_MAXVA = ..RETADR + .BYTES - 1;      ! SPACE ALLOCATED
: 199      0310      IF .RETADR[0] LSSU .LNK$GL_MINVA      ! AND RESET MINIMUM IF NEXCESSARY
: 200      0311      THEN LNK$GL_MINVA = .RETADR[0];
: 201      0312      RETURN TRUE      ! RETURN SUCCESS
: 202      0313      END;
: 203      0314 1 RETURN FALSE      ! NO MEMORY FOUND
: 204      0315 1 END;      ! END OF LNK$FINDVIRMEM
```

```

53      08      56 00000000' 007C 00000      .ENTRY LNK$FINDVIRMEM, Save R2,R3,R4,R5,R6      : 0252
      AC      09 7E 00002      MOVAB LNK$GL_MAXVA, R6
      04      06 12 0000E      ASHL #9, PAGES, BYTES      : 0279
      0093 31 00013      BNEQ 1$
      03      0C      04      0093 31 00013      CLRL @RETADR      : 0281
      50      06 91 00016 1$:      BRW 12$      : 0282
      05 1E 00019      CMPB (AP), #3      : 0285
      01 D0 0001B      BGEQU 2$
      09 11 0001E      MOVL #1, R0
      50 D4 00020 2$:      BRB 3$
      0C      AC D5 00022      CLRL R0
      02 12 00025      TSTL 12(AP)
      50 D6 00027      BNEQ 3$
      51 D4 00029 3$:      INCL R0
      0C      AC D5 0002B      CLRL R1
      02 13 0002E      TSTL LOWESTVA
      51 D6 00030      BEQL 4$
      50 CB 00032 4$:      INCL R1
      54 00000000G 00 9E 00036      BICL3 R0, R1, SPECIALADR
      52      64 D0 0003D 5$:      MOVAB LNK$GL_FVMLST, PREVBLK
      55      6B 13 00040      MOVL (PREVBLK), FREBLK
      0C      1F      04      55      55      6B 13 00040      BEQL 13$
      AC      04      A2 D1 00045      BLBC SPECIALADR, 6$
      1E 1F 0004A      CMPL 4(FREBLK), LOWESTVA
      51      04      08      51      51      12 1F 00056      BLSSU 7$
      0C      AC      08      51      51      12 1F 00056      ADDL3 8(FREBLK), 4(FREBLK), R1
      50      53      0C      51      51      12 1F 00056      CMPL R1, LOWESTVA
      50      51      0C      51      51      12 1F 00056      BLSSU 7$
      0D 1E 00060      ADDL3 LOWESTVA, BYTES, R0
      06 11 00062      CMPL R1, R0
      08      A2      53 D1 00064 6$:      BGEQU 8$
      05 1B 00068      BRB 7$
      54      52 D0 0006A 7$:      BLEQU 8$
      CE 11 0006D      MOVL FREBLK, PREVBLK
      04      BC      04      53      53      CE 11 0006D      BRB 5$
      08      A2      04      53      53      CE 11 0006D      MOVL 4(FREBLK), @RETADR
      64      62 D0 0007A 8$:      SUBL2 BYTES, 8(FREBLK)
      52 DD 0007D      BNEQ 9$
      0C DD 0007F      MOVL (FREBLK), (PREVBLK)
      02 FB 00081      PUSHL FREBLK
      04 11 00088      PUSHL #12
      53 C0 0008A 9$:      CALLS #2, LNK$DEALBLK
      00      02 FB 00081      BRB 10$
      04      04      53      04      53      02 FB 00081      ADDL2 BYTES, 4(FREBLK)
      00      04      53      04      53      02 FB 00081      : 0300
      04      53      04      53      53      02 FB 00081      : 0307
```

LNK_VMCTRL
V04=000

K 4
16-Sep-1984 00:39:52
14-Sep-1984 12:40:38

VAX-11 Bliss-32 V4.0-742
[LINKER.SRC]LNKVMCTRL.B32;1

Page 8
(4)

	50	04	BC	D0	0008E	10%:	MOVL	@RETADR, R0	:	0308
	51	FF	A340	9E	00092		MOVAB	-1(BYTES)[R0], R1	:	
	66		51	D1	00097		CMPL	R1, LNK\$GL_MAXVA	:	
			03	1B	0009A		BLEQU	11%	:	
	66		51	D0	0009C		MOVL	R1, LNK\$GL_MAXVA	:	0309
FC	A6		50	D1	0009F	11%:	CMPL	R0, LNK\$GL_MINVA	:	0310
			04	1E	000A3		BGEQU	12%	:	
FC	A6		50	D0	000A5		MOVL	R0, LNK\$GL_MINVA	:	0311
	50		01	D0	000A9	12%:	MOVL	#1, R0	:	0312
			04	000AC			RET		:	
			50	D4	000AD	13%:	CLRL	R0	:	0314
			04	000AF			RET		:	0315

; Routine Size: 176 bytes, Routine Base: \$CODE\$ + 00C1

; 205 0316 0 END ELUDOM

PSECT SUMMARY

Name	Bytes	Attributes
\$GLOBALS	8	NOVEC, WRT, RD, NOEXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(2)
\$CODE\$	369	NOVEC, NOWRT, RD, EXE, NOSHR, LCL, REL, CON, NOPIC, ALIGN(2)

Library Statistics

File	Total	Symbols Loaded	Percent	Pages Mapped	Processing Time
\$255\$DUA28:[SYSLIB]STARLET.L32;1	9776	6	0	581	00:01.0
\$255\$DUA28:[LINKER.OBJ]DATBAS.L32;1	538	4	0	28	00:00.5

COMMAND QUALIFIERS

; BLISS/CHECK=(FIELD,INITIAL,OPTIMIZE)/LIS=LIS\$:LNKVMCTRL/OBJ=OBJ\$:LNKVMCTRL MSRC\$:LNKVMCTRL/UPDATE=(ENH\$:LNKVMCTRL)

; Size: 369 code + 8 data bytes
; Run Time: 00:09.7
; Elapsed Time: 00:29.0
; Lines/CPU Min: 1962
; Lexemes/CPU-Min: 17180
; Memory Used: 94 pages

LNK_VMCTRL
V04=000

⁴
16-Sep-1984 00:39:52

VAX-11 Bliss-32 V4.0-742

Page 9

; Compilation Complete

_ \$2

Vir
Sta
Ima
Ima
Ima
Num
Num
Num
Num
Num
Num
Ima
Map
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Per

Tot
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85

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0220

AH-BT13A-SE
 VAX/VMS V4.0

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